

Sonali Deore

"NIRMALA", PLOT NO. 10, BEHIND RESHIMBANDH
MANGAL KARYALAYA, NEAR THAKKAR ROW HOUSES,
SHIVKRIPA NAGAR, HIRAWADI,
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Professional Objective:

To obtain a position that offers challenge and career opportunity for professional growth in a people-oriented organization where advancement in career is based upon skills and contributions.

Academic Details:

- M.E. in Electronics (2016) from M.G.M.'s College Of Engineering, Nanded with 72.23% (Distinction).
- B.Tech in Electronics and Telecommunication stream (2009) from S.G.G.S.I.E. & T. Nanded with C.G.A. 6.66 (1st Class).

Employment Details:

Teaching Experience	5 Years
Current Designation	Assistant Professor (Department of Artificial Intelligence & Data Science Engineering)
Name of Employer	Matoshri College of Engineering and Research Centre, Nashik
Date of Joining: (current institute)	01 st August 2022
Earlier Employment Record	<ul style="list-style-type: none">• From 1st July 2019 to till date, working as Executive Secretary in Nashik Industries and Manufacturers' Association.• From 5th February 2019 to till date, working as a Co-coordinator of NIMA-SGGS Start-Up/Entrepreneurship Help desk.• From 14th June 2018 to 15th December 2018, working as an Assistant Professor in Pune Vidyarthi Griha's Collage of Engineering, Nasik, Maharashtra.• From 31st Dec 2015 to 20th March 2018, working as a Lecturer in Govt. Polytechnic Nasik, Maharashtra.• From 15th June 2009 to 15th May 2011, working as a Lecturer in K.K.Wagh Polytechnic Chandori, Nasik, Maharashtra.

Contribution In University /College work:	<ul style="list-style-type: none"> ➤ Actively participating and leading social events of Department as well as Institute level. ➤ Member in college organizing team to explore lecture series of ISTE,FDP,IETE programs. ➤ Organizer in various events of Institute Level. ➤ Event organizer in all school and college annual day functions and other technical events.
Attended & Coordinated workshop/training:	<ul style="list-style-type: none"> • Attended one day National Conference on Advancement in Medical in 2009. • Attended STTP on VLSI design in Amrutvahini Polytechnic in 2010.
Google scholar Link	=
Website	www. engg.matoshri.edu.in
Weblinks	https://engg.matoshri.edu.in/academics/aids