

Mr. Pankaj Ashok Nawale

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**Professional Objective:**

Seeking a challenging career in an organization to utilize my knowledge to its full potential & where my abilities and skill would be an asset.

Academic Details:

- Masters in Electronics Engineering (Digital Systems) from S.R.E.S. College of Engineering, Kopargaon under SPPU, Pune.
- Bachelor of Electronics and Telecommunication Engineering from S.R.E.S. College of Engineering, Kopargaon under SPPU, Pune.

Employment Details:**Teaching Experience:****10 Years****Current Designation**

Assistant Professor (Department of E&Tc Engineering)

Name of Employer:

Matoshri College of Engineering and Research Center, Nashik

Date of Joining: (current institute)1st July 2011.**Interest**

Microcontrollers , Embedded system and IoT

Projects Undertaken :

- Design and Improvement of microstrip patch antenna parameters using DGS. M.E.(Electronics)
- GSM based multi security system . B.E. (E & TC)
- Computerized industrial equipment control. T.E. (E & TC) .

Awards:

- Won 1st prize in Paper presentation on Topic "Improvement Of Microstrip Patch Antenna Parameters Using Defected Ground Structure" in event "TECHNOSPHERE" at College of Engineering , Kopargaon, in 2013.

Other Experience:

- Worked as External and Internal Senior Supervisor for SPPU Examinations.
- Worked as judge for State level Project Competition MAP-FEAST 2019 organized by Matoshri Asrabai Polytechnic , Nashik.

Association with Professional Societies

- Member of Institution of Engineers , India

Invited Talks

- Delivered expert lecture on Embedded System for Third year Students of E&TC from Matoshri Asrabai Polytechnic , Nashik, on 27 Feb 2018.

Subjects taught	<ul style="list-style-type: none"> • Microcontroller and Applications. • Advanced Processors -ARM • Embedded systems and RTOS . • Internet of Things • Network Synthesis and filter Design . • Wave-theory and Antennas
MOOC/ AICTE FDPs/ Certificate Courses	
NPTEL	<p>Successfully completed NPTEL online certification- for subject “Python For data science “</p> <p>Successfully completed NPTEL online certification- for subject “Microprocessors and Microcontroller”</p> <p>Successfully completed NPTEL online certification- “Teaching And Learning in Engineering”</p> <p>Successfully completed NPTEL online certification for subject “Satellite Communication”</p> <p>Successfully completed NPTEL online certification for subject “Introduction to Research”</p> <p>Successfully completed NPTEL online certification for subject “Basic electronics”</p>
Coursera	Completed online course by course-era on “Introduction and Programming with IoT Boards” by Pohang university of Science and Technology in Dec 2020 .
	<p>📄 Conferences/Workshop/Seminars/STTPattended: 10</p> <p>📄 Conferences/WorkshopsOrganized: 01</p>
Publications/ Presentations	<ul style="list-style-type: none"> • Participated in Paper presentation on Topic “ Microwave Photonics ” in international conference ICRJET at SNJBCOE , Chandwad from 22 to 24 Feb2013. • Won 1 St prize in Paper presentation on Topic “Improvement Of Microstrip Patch Antenna Parameters Using Defected Ground Structure” in event “TECHNOSPHERE” at College of Engg, Kopergaon, from 10 to 12 Jan 2014. • Participated in Paper presentation on Topic “Design and Improvement of Microstrip Patch Antenna Parameters Using Defected Ground Structure” in e-PGCON2014 at Rasoni COE, wagholi, pune . • Papers Published on topic “Design and Improvement of Microstrip Patch Antenna Parameters Using Defected Ground Structure” , in Int. Journal of Engineering Research and Applications Vol. 4, Issue 6(Version 3), pp.123-129. June 2014 . • Papers Published on topic “Rectangular Microstrip Patch Antenna For 2.4 GHzCommunication Using Defected Ground Structure. In International Journal of Advance Foundation and Research in Computer Volume 2, Issue 1., ISSN 2348 – 4853, January 2015. • Participated in Paper presentation on Topic “ Review on methods of improving micro strip patch antenna parameters using DGS as metamaterial” in ICCCE- 2017 at MCOERC , Nashik in 18th to 19 August 2017.

	<ul style="list-style-type: none"> Participated in Paper presentation on Topic “A Review of Microstrip Patch Antenna Parameters Improvement using Defected Ground Structure” in ICCCC- 2019 at MCOERC , Nashik in August 2019.(ID ETC 730)
Conferences/Workshop/Seminars/STTP attended:	<ul style="list-style-type: none"> Attended three days faculty orientation workshop on “Microcontrollers ”. on revised syllabus of T.E. (E&Tc) 2021 , organized by Dept. of E&TC Engineering, STES, Sinhgad Institute of Technology, Lonavala . Attended eight days faculty orientation workshop on” Role of Teacher: To boost Moral values, Ethics in Engineering Education System’ organized by KBT COE Nashik Attended six days faculty orientation workshop on “python 3.4.3”organized by Visvesvaraya Institute of Technology, Nashik. Attempted quiz on Industry 4.0 organized by Department of mechanical engineering of SRESCOE, Kopargaon on 6th May 2020. Attended FDP on Cypress- Programmable SoC held at Bharati Vidyapeeth University COE, Pune . Attended FDP on Digital signal Processors on 29 th November 2017.at KKWCOERC, Nashik , Attended five days faculty development program on ‘Quality improvement In academics’ at MCOERC, Nashik.from 19to 23 Dec 2011. Attended faculty orientation workshop on “Embedded System and RTOS”. on revised syllabus of B.E. (E&Tc)2015. Attended three days faculty orientation workshop on “Microcontroller and applications”at MIT Pune on revised syllabus of T.E. (E & TC)2014. Attended three days national level seminar on” Design and analysis of microwave antennae.” At AVCOE, Samgamner. From 27 to 29Jan 2015. Attended faculty orientation workshop on “Electronics and Electrical Engg”. on revised syllabus of S.E. (Mech)13th Dec 2013.
Weblinks:	
Website	https://engg.matoshri.edu.in/academics/electronics
LinkedIn	https://www.linkedin.com/in/pankaj-nawale-0522971a3/
Google Scholar	https://scholar.google.com/citations?user=YDWmKcYAAAAJ&hl=en&authuser