

Dr. Devidas Dnyaneshwar Dighe

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**Professional Objective:**

Secure a responsible career opportunity to fully utilize my knowledge and skills, while making a significant contribution to the success of the Institute. Aside from providing high-quality teaching has key interest and undertakes challenges in administration, academics, and research. Eager to help the students to develop their skills and abilities, provide counseling, assist students in reaching their potential, and ensure the learning environment is appropriate and positive.

Academic Details:

- Submitted Thesis for Doctorate in Electronics Engineering from Sant Gadge Baba Amravati University, Amravati
- Master of Electronics Engineering (Digital systems) from Government College of Engineering, Pune
- Bachelor of Electronics Engineering from Savitribai Phule Pune University, Pune

Employment Details:

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| Teaching Experience: | 24 Years |
| Current Designation | Professor and Head (Department of Electronics And Telecommunication Engineering) |
| Name of Employer: | Matoshri College of Engineering and Research Center, Nashik |
| Date of Joining: (current institute) | 1 st August 2008 |
| Earlier Employment Record | 1 year as an instrumentation engineer in Indigenous control Pvt. Ltd, Mumbai, then 10 years of teaching experience as Assistant Professor and Associate Professor at Department of Electronics And Telecommunication Engineering, Amrutvahini college of engineering, Sangamner thereafter 1 ½ years of teaching experience as Associate Professor at Department of Electronics And Telecommunication Engineering, MET's Institute of Engineering, Nashik. |
| Research Interest | Signal Processing, Image Processing, and Digital systems |
| PhD Work Title | “Reconstruction Of High Resolution Image Using Low Resolution Color Image(S) For Quality Improvement” |
| Doctoral Advisor | Dr. Gajanan K. Kharate, Principal, Matoshri College of Engineering and Research Centre, Nashik. Ex Dean and Chairman, BoS (E&TC), SPPU. |
| Various Bodies at Affiliated University- Savitribai Phule Pune University (SPPU) | <ul style="list-style-type: none"> • Served as Chairman & Member, Selection Committee, Chairman & Member, Local Inquiry Committee, Examination Committee at SPPU |
| IPR (Patent -01) | Patents: Filed Patent “ Low Cost Weather Monitoring Station using RASPBERRY PI ”, Status- Published, Number 201721031155A |

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| Association with Professional Societies | <ul style="list-style-type: none"> Fellow member of Institution of Engineers (IEI) (membership number M-157158) Ex-Student Counselor for IEEE, student branch associated with Amrutvahini College of Engineering Sangamner. Chapter for 2007-08. |
| Association with Conferences and Journals | Served as Convener, member Program Committee, Reviewer and Session Chair for National and International Conferences. |
| Invited Talks | Delivered talk for FDP & orientation Programs of Faculty, for the Course of Digital Electronics of Savitribai Phule Pune University for 2012 as well as for 2015 curriculum. |
| MOOC/ AICTE FDPs/ Certificate Courses | |
| NPTEL | Successfully completed Spoken Tutorial of IIT Bombay “ One Week Online Faculty Development Program on PYTHON 3.4.3” |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “Satellite communication” with 84% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “ Research Writing ” with 100% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “NBA Accreditation and Teaching-Learning in Engineering (NATE)” with 98% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “Introduction to Research ” (Top 5%) with 85% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “Switching Circuits and Logic Design ” with 83% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “Patent Drafting for Beginners ” with 68% |
| | Successfully completed NPTEL online certification- AICTE approved FDP titled “ Fundamental of Electronic Device Fabrication ” (Top 5%) with 86% |
| Coursera | “Digital Systems: From Logic Gates to Processors” -an online non-credit course authorized by Universitat Autònoma de Barcelona and offered through Coursera with 100% |
| | “COVID-19: What You Need to Know (CME Eligible)” an online non-credit course authorized by Osmosis and offered through Coursera |
| | <ul style="list-style-type: none"> Conferences/Workshop/Seminars/ STTP attended: 25 Conferences/Workshops/ Technical Events Organized: 10 |
| Publications: | |
| Articles at Magazine : | 02 |
| Research Papers (40) | <ul style="list-style-type: none"> International Journal = 34 International Conference = 04 National Conference = 02 |
| Weblinks: | |
| Website | https://engg.matoshri.edu.in/academics/electronics |
| Orchid ID | https://orcid.org/0000-0003-3965-2666 |
| LinkedIn | https://www.linkedin.com/in/devidas-dighe-b549a262/?originalSubdomain=in |
| Research Gate | https://www.researchgate.net/profile/Devidas_Dighe |
| Google Scholar | https://scholar.google.co.in/citations?user=GDqYX7kAAAAJ&hl=en |