



Matoshri Education Society's

Matoshri College of Engineering and Research Centre, (Autonomous)
Eklahareshtar, Near Odhagaon, Opp Nashik-Aurangabad Highway, Nashik-422105

Electronics and Telecommunication Engineering & Electronics and Computer Engineering Department

Date: 14/10/2025

Board of Studies Minutes of Meeting

(Board of Studies 3rd Meeting)

Meeting Overview

The meeting of the Board of Studies (BoS) for the Department of Electronics and Communication Engineering was organised on 13/10/2025 at 3.00 pm to discuss and approve the structure of the FE to BE Apprenticeship Embedded Degree Programme (AEDP) in Electronics and Communication Engineering. The meeting was organized at short notice as the institute has been selected by Savitribai Phule Pune University, under the directions of the Ministry of Higher Education, to implement the AEDP initiative.

The objective of the meeting was to deliberate upon and finalize the academic structure and implementation framework of the AEDP programme. The meeting was attended by the honourable Director, academic experts, industry representatives, Heads of Department, and faculty members of the institute. The meeting was attended by distinguished academicians, university nominees, industry representatives, alumni members, and faculty members of the Electronics and Telecommunication Engineering Department.

The meeting commenced with a formal welcome address delivered by Dr. Jayant J. Chopade, Head of the Department of Electronics and Telecommunication Engineering and Chairman, Board of Studies (BoS).

Sr. No.	Name of Person	Designation	Role in BOS	Attendance
1.	Dr. Jayant J. Chopade	Head of the Department (E&TC / E&C Engg.)	Chairman	Present
2.	Dr. Gajanan K Kharate	Director	Invitee	Present
3.	Dr. Varsha H Patil	Vice-Principal & Academic Dean	Invitee	Present
4.	Dr. Devidas D. Dighe	Dean Examination (E&TC Engg.)	CEO	Present
5.	Dr. Ravindra G. Dabhade	Associate Professor- E&TC Engg. Specialization : Electronics Engineering	Member	Present
6.	Dr. Dnyaneshwar D. Ahire	Associate Professor- E&TC Engg. Specialization: E&TC Engg.	Member	Present
7.	Mr. Vishwas R. Wadekar	Associate Professor- E&TC Engg. Specialization : Communication Engineering	Member	Present

8.	Mr. Pankaj A. Nawale	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
9.	Mr. Bhagwan S. Bodke	Assistant Professor- E&TC Engg. Specialization: Control systems Engineering	Member	Present
10.	Mr. Sachin B. Borse	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
11.	Mrs. Manjusha S. Borse	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
12.	Ms. Revati C. Rathi	Assistant Professor- E&TC Engg. Specialization: - E&TC Engg.	Member	Present
13.	Ms. Madhuri S. Nimse	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
14.	Ms. Nikita B. Aher	Assistant Professor- E&TC Engg. Specialization: Computer Engineering	Member	Present
15.	Ms. Utkarsha H. Holkar	Assistant Professor- E&TC Engg. Specialization: VLSI & Embedded Systems	Member	Present
16.	Ms. Harsha P. Bhamre	Assistant Professor- E&TC Engg. Specialization: Microwave Engineering	Member	Present
17.	Mrs. Vidya Vishwas Gite	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
18.	Mr. Mangesh S. Wadatkar	Assistant Professor- E&TC Engg. Specialization: Electronics	Member	Present
19.	Dr. Sanjiv. V Bonde	Professor, Department of Electronics & Telecomm. Engg. SGGS, Nanded	Other University Representative	Present
20.	Dr. Sanjay L. Nalbalwar	Professor & Head, Department of Electronics & Telecomm. Engg. Dr. Babasaheb Ambedkar Technological University, Lonere, Tal.: Mangaon Dist.: Raigad, M.S., Pin: 402103,	Other University Representative	Present
21.	Dr. S. D. Shirbhadurkar	Principal, Dr. D. Y. Patil Technical Campus Varale, Talegaon, Pune	Pune University Representative	Present
22.	Mr. Anand S. Gharpure	Designation: Director Affiliation: Sonic Multitech Private Limited Name of Industry with address: Sonic Multitech Private Limited, Nashik	Industry Representative	Present
23.	Mr. Kiran More	Designation: General Manager(Projects) Affiliation: ProNET Technologies Pvt Ltd	Postgraduate alumnus	Present

Minutes of the Meeting (MoM)

Agenda I: Confirmation of minutes of Second BoS Meeting and Actions Taken

The Minutes of the previous Board of Studies meeting along with the corresponding Action Taken Report (ATR) were presented before the Board of studies for review. The members examined the proceedings and confirmed the recorded minutes and implementation status of the action items without modifications.

Resolution

The minutes of meeting confirmed and ATR accepted.

Agenda II: Approval of Proposed Curriculum Structure (UG – AEDP) as per NEP Guidelines

The proposed curriculum structure for Undergraduate programmes under the Apprenticeship Embedded Degree Program (AEDP), aligned with NEP guidelines, was presented before the Board for detailed discussion and approval.

After thorough deliberation, the curriculum structure was unanimously approved by the Board of Studies.

Resolution

The Curricula structures were accepted unanimously by the BoS members.

Agenda III: Approval of First Year B.Tech. AEDP Courses

The detailed curriculum for the First Year B.Tech. Programme in Electronics & Computer Engineering under ADEP was presented before the Board for review.

The Board members carefully examined the curriculum and provided valuable feedback and suggestions for improvement.

After discussion, the proposed curriculum was approved with minor revisions as suggested during the meeting.

Resolution

The Curricula structures were accepted unanimously by the BoS members.

Agenda IV: Suggestions and Feedback from Members

During the meeting, the following suggestions and feedback were received:

Dr. S. D. Shirbhadurkar suggested assigning 50% weightage to the In-semester examination and 50% weightage to the End-semester examination.

Response: It was informed that this suggestion has been forwarded to the Academic Council for further discussion and appropriate decision.

Dr. Sanjiv Bonde appreciated the syllabus structure of the Second Year M.Tech. Programme.

Mr. Anand S. Gharpure suggested inclusion of assessment integration in the evaluation system.

Response: It was clarified that assessment integration will include internal assessments, theory examinations, laboratory work, and project submissions, ensuring continuous evaluation of students' performance for holistic development.

Mr. Kiran More appreciated the structure of the First Year B.Tech. ADEP syllabus.

Agenda V: Vote of Thanks

The meeting concluded with a formal Vote of Thanks proposed by Mr. Vishwas R. Wadekar.

He expressed sincere gratitude to all distinguished guests, Board members, experts, and faculty participants for their valuable insights, constructive suggestions, and active participation in the meeting.


Dr. Jayant J. Chopade
Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
Matoshri College of Engineering & Research Centre

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J Jayant Chopade (Presenting)

What is AEDP ?

- ✓ A new model of degree program introduced by the Ministry of Skill Development & Entrepreneurship (MSDE).
- ✓ Combines academic learning with industry apprenticeship.
- ✓ Aims to make students job-ready while studying.

Empowering Students through Learning and Practice.

3:07 PM | nvu-esqt-prx

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People

- Save attendance
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- Add people
- SANJIV BONDE
- Suresh Shrivastava
- Sunil Kamtapurkar
- Utkarsha Rahane
- Vaishali Senap
- Vishwas Wadkar

3:49 PM | nvu-esqt-prx

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**Electronics and Telecommunication Engineering &
Electronics and Computer Engineering Department**

Date: 21/10/2025

Action Taken Report (ATR) Board of Studies Third Meeting

Sr. No.	Discussion / Agenda Item	Resolution	Action Taken	Status
1	Welcome, Introduction and Overview of Department	The Board acknowledged the presentation of departmental overview and introduction of BoS members and experts.	Departmental academic overview and BoS member introduction were formally presented and recorded in the minutes.	Complied
2	Approval of UG (ADEP) Curriculum Structure as per NEP Guidelines	The proposed UG ADEP curriculum structure was unanimously approved by the Board of Studies.	The UG ADEP curriculum framework was reviewed, discussed, and approved for implementation as per NEP guidelines.	Complied
3	Approval of First Year B.Tech. ADEP Curriculum	The proposed First Year B.Tech. ADEP curriculum was approved with minor revisions.	Suggestions from BoS members were incorporated and the revised curriculum was finalized for submission to Academic Council.	Complied
4	Examination Weightage (50% Insem & 50% Endsem)	The suggestion was forwarded to Academic Council for further consideration.	Proposal regarding revised assessment weightage was documented and submitted to Academic Council for review and decision.	Under Process
5	Assessment Integration in Evaluation System	The suggestion regarding integrated assessment system was accepted conceptually.	Continuous assessment model including internal tests, lab work, and project evaluation was recorded for implementation refinement.	Complied


Dr. Jayant J. Chopade

Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
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