



Matoshri Education Society's

Matoshri College of Engineering and Research Centre, (Autonomous)
Eklahareshtar, Near Odhagaon, Opp Nashik-Aurangabad Highway, Nashik-422105

Electronics and Telecommunication Engineering & Electronics and Computer Engineering Department

Date: 10/04/2025

Board of Studies Minutes of Meeting

(Board of Studies 2nd Meeting)

Meeting Overview

The meeting of the Board of Studies (BoS) for the Department of Electronics and Telecommunication Engineering was conducted on 8th April 2025 at 3.00 PM through the online mode using the Google Meet platform. The meeting was organized with the objective of reviewing and deliberating upon the proposed courses syllabus of First Year, Curriculum structure for Second Year B.Tech. and Second Year M.Tech. Programmes, Open Elective Courses, and academic initiatives in accordance with the National Education Policy (NEP) guidelines.

The meeting was attended by distinguished academicians, university nominees, industry representatives, alumni members, and faculty members of the Electronics and Telecommunication Engineering Department.

Sr. No.	Name of Person	Designation	Role in BOS	Attendance
1.	Dr. Jayant J. Chopade	Head of the Department (E&TC / E&C Engg.)	Chairman	Present
2.	Dr. Gajanan K Kharate	Director	Invitee	Present
3.	Dr. Varsha H Patil	Vice-Principal & Academic Dean	Invitee	Present
4.	Dr. Devidas D. Dighe	Dean Examination (E&TC Engg.)	CEO	Present
5.	Dr. Ravindra G. Dabhade	Associate Professor- E&TC Engg. Specialization : Electronics Engineering	Member	Present
6.	Dr. Dnyaneshwar D. Ahire	Associate Professor- E&TC Engg. Specialization: E&TC Engg.	Member	Present
7.	Mr. Vishwas R. Wadekar	Associate Professor- E&TC Engg. Specialization : Communication Engineering	Member	Present
8.	Mr. Pankaj A. Nawale	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
9.	Mr. Bhagwan S. Bodke	Assistant Professor- E&TC Engg. Specialization: Control systems Engineering	Member	Present

10.	Mr. Sachin B. Borse	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
11.	Mrs. Manjusha S. Borse	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
12.	Ms. Revati C. Rathi	Assistant Professor- E&TC Engg. Specialization: - E&TC Engg.	Member	Present
13.	Ms. Madhuri S. Nimse	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
14.	Ms. Nikita B. Aher	Assistant Professor- E&TC Engg. Specialization: Computer Engineering	Member	Present
15.	Ms. Utkarsha H. Holkar	Assistant Professor- E&TC Engg. Specialization: VLSI & Embedded Systems	Member	Present
16.	Ms. Harsha P. Bhamre	Assistant Professor- E&TC Engg. Specialization: Microwave Engineering	Member	Present
17.	Mrs. Vidya Vishwas Gite	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
18.	Mr. Mangesh S. Wadatkar	Assistant Professor- E&TC Engg. Specialization: Electronics	Member	Present
19.	Dr. Sanjiv. V Bonde	Professor, Department of Electronics & Telecomm. Engg. SGS, Nanded	Other University Representative	Present
20.	Dr. Sanjay L. Nalbalwar	Professor & Head, Department of Electronics & Telecomm. Engg. Dr. Babasaheb Ambedkar Technological University, Lonere, Tal.: Mangaon Dist.: Raigad, M.S., Pin: 402103,	Other University Representative	Present
21.	Dr. S. D. Shirbhadrurkar	Principal, Dr. D. Y. Patil Technical Campus Varale, Talegaon, Pune	Pune University Representative	Present
22.	Mr. Anand S. Gharpure	Designation: Director Affiliation: Sonic Multitech Private Limited Name of Industry with address: Sonic Multitech Private Limited, Nashik	Industry Representative	Present
23.	Mr. Kiran More	Designation: General Manager (Projects) Affiliation: ProNET Technologies Pvt Ltd	Postgraduate alumnus	Present

Minutes of the Meeting (MoM)

The meeting of the Board of Studies (BoS) for the Department of Electronics and Telecommunication Engineering was conducted on 20/07/2024 at 3.00 PM through the online mode using the Google Meet platform.

The meeting commenced with a formal welcome address delivered by Dr. Jayant J. Chopade, Head of the Department of Electronics and Telecommunication Engineering and Chairman, Board of Studies (BoS).

Agenda I: Confirmation of minutes of first BoS Meeting and Actions Taken

The Minutes of the previous Board of Studies meeting along with the corresponding Action Taken Report (ATR) were presented before the Board of studies for review. The members examined the proceedings and confirmed the recorded minutes and implementation status of the action items without modifications.

Resolution

The minutes of meeting confirmed and ATR accepted.

Agenda II: Approval of Proposed courses of First Year and Curriculum Structure (UG and PG) as per NEP Guidelines

The proposed courses of First Year and Curriculum structure for both Undergraduate and Postgraduate programmes, aligned with the National Education Policy (NEP) guidelines, was presented before the Board for detailed discussion and approval.

After thorough deliberation, the curriculum structure was unanimously approved by the Board of Studies.

Agenda III: Approval of Proposed Curriculum for Second Year B.Tech. Courses

The detailed curriculum for the Second Year B.Tech. Programme was presented before the Board for academic review.

The Board members thoroughly examined the curriculum structure and provided valuable feedback and suggestions for improvement.

After detailed discussion, the proposed curriculum was approved with minor revisions as discussed during the meeting.

Agenda IV: Approval of Proposed Curriculum for Second Year M.Tech. Courses

The proposed curriculum for the Second Year M.Tech. Programme was presented before the Board for approval.

After review and discussion, the curriculum was approved with the consensus of all members.

Agenda V: Approval of Open Elective Courses as per NEP Guidelines

The Open Elective Courses under NEP framework were presented before the Board for approval.

After discussion, the courses were approved by the Board with consensus of all members, recognizing their importance in providing interdisciplinary learning opportunities.

Agenda VI: Suggestions and Feedback from Members

During the meeting, the following suggestions and feedback were received from the members:

Mr. Anand Gharpure suggested inclusion of basic study of VHDL Language in the Digital Logic Design course of the Second Year B.Tech. Curriculum.

Response: It was clarified that the basics of VHDL Language have already been included in the course, and further detailed understanding will be covered in the VLSI course to ensure comprehensive learning.

Mr. Kiran More appreciated the inclusion of AI-based subjects in the syllabus, highlighting the department's commitment toward innovation and emerging technologies.

Dr. Sanjiv Bonde suggested a proposed name change for M-III course to enhance its relevance and international academic appeal.

Response: The suggestion has been forwarded to the Academic Council for further consideration and decision.

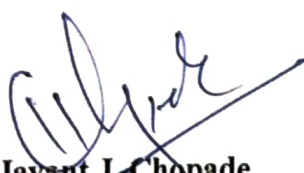
Dr. Sanjay L. Nalbalwar appreciated the structure of the Second Year M.Tech. Syllabus.

Dr. S. D. Shirbhadurkar appreciated the syllabus structure of both Second Year B.Tech. and Second Year M.Tech. Programmes.

Agenda VII: Vote of Thanks

The meeting concluded with a formal Vote of Thanks proposed by Mr. Vishwas R. Wadekar.

He expressed sincere gratitude to all distinguished members, experts, industry representatives, alumni members, and faculty for their valuable suggestions, constructive feedback, and active participation in the meeting.



Dr. Jayant J. Chopade
Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
Matoshri College of Engineering & Research Centre



Matoshri Education Society's

Matoshri College of Engineering and Research Centre, (Autonomous)
Eklareshwar, Near Odhagaon, Opp Nashik-Aurangabad Highway, Nashik-422105

**Electronics and Telecommunication Engineering &
Electronics and Computer Engineering Department**

Date: 18/04/2025

Action Taken Report (ATR) Board of Studies Second Meeting

Sr. No.	Discussion / Agenda Item	Resolution	Action Taken	Status
1	Approval of UG and PG Curriculum Structure as per NEP Guidelines	The curriculum structure for UG and PG programmes was unanimously approved by the Board.	The curriculum framework was reviewed and approved in accordance with NEP guidelines.	Complied
2	Approval of Second Year B.Tech. Curriculum	The curriculum was approved with minor revisions.	Suggestions on VHDL inclusion and curriculum refinement were incorporated and documented.	Complied
3	Approval of Second Year M.Tech. Curriculum	The curriculum was approved with consensus.	The curriculum structure was finalized and forwarded for implementation.	Complied
4	Approval of Open Elective Courses	Open Elective Courses were approved by the Board.	Interdisciplinary electives were documented and approved for implementation.	Complied
5	Faculty Suggestions and Feedback	Suggestions were noted and resolved/forwarded appropriately.	Academic Council reference made for M-III course name change proposal.	Under Process


Dr. Jayant J. Chopade

Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
Matoshri College of Engineering & Research Centre