



Matoshri Education Society's

Matoshri College of Engineering and Research Centre, (Autonomous)

Eklahareshivar, Near Odhagaon, Opp Nashik-Aurangabad Highway, Nashik-422105

Electronics and Telecommunication Engineering & Electronics and Computer Engineering Department

Date: 22/07/2024

Board of Studies Minutes of Meeting

(Board of Studies 1st Meeting)

Meeting Overview

The first meeting of the Board of Studies (BoS) for the Department of Electronics and Telecommunication Engineering and Electronics & Computer department was arranged on 20th July 2024 at 3.00 PM through the online mode using the Google Meet platform. The meeting was organized with the objective of reviewing and deliberating upon the proposed curriculum structure for Undergraduate and Postgraduate programmes, course frameworks, and academic initiatives in accordance with the National Education Policy (NEP) guidelines.

The meeting was attended by distinguished academicians, university nominees, industry representatives, alumni members, and faculty members of the Electronics and Telecommunication Engineering Department.

S.N.	Name of Person	Designation	Role in BOS	Attendance
1.	Dr. Jayant J. Chopade	Head of the Department (E&TC / E&C Engg.)	Chairman	Present
2.	Dr. Gajanan K Kharate	Director	Invitee	Present
3.	Dr. Varsha H Patil	Vice-Principal & Academic Dean	Invitee	Present
4.	Dr. Devidas D. Dighe	Dean Examination (E&TC Engg.)	CEO	Present
5.	Dr. Ravindra G. Dabhade	Associate Professor- E&TC Engg. Specialization: Electronics Engineering	Member	Present
6.	Dr. Dnyaneshwar D. Ahire	Associate Professor- E&TC Engg. Specialization: E&TC Engg.	Member	Present
7.	Mr. Vishwas R. Wadekar	Associate Professor- E&TC Engg. Specialization : Communication Engineering	Member	Present
8.	Mr. Pankaj A. Nawale	Assistant Professor- E&TC Engg. Specialization: Digital Systems	Member	Present
9.	Mr. Bhagwan S. Bodke	Assistant Professor- E&TC Engg. Specialization: Control systems Engineering	Member	Present

10.	Ms. Revati C. Rathi	Assistant Professor- E&TC Engg. Specialization: - E&TC Engg.	Member	Present
11.	Ms. Madhuri S.Nimse	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
12.	Ms. Nikita B. Aher	Assistant Professor- E&TC Engg. Specialization: Computer Engineering	Member	Present
13.	Ms. Utkarsha H. Holkar	Assistant Professor- E&TC Engg. Specialization: VLSI & Embedded Systems	Member	Present
14.	Ms. Harsha P. Bhamre	Assistant Professor- E&TC Engg. Specialization: Microwave Engineering	Member	Present
15.	Mrs. Vidya Vishwas Gite	Assistant Professor- E&TC Engg. Specialization: Communication Engineering	Member	Present
16.	Mr. Mangesh S. Wadatkar	Assistant Professor- E&TC Engg. Specialization: Electronics	Member	Present
17.	Dr. Sanjiv. V Bonde	Professor, Department of Electronics & Telecomm. Engg. SGGS , Nanded	Other University Representative	Present
18.	Dr. Sanjay L. Nalbalwar	Professor & Head, Department of Electronics & Telecomm. Engg. Dr. Babasaheb Ambedkar Technological University, Lonere, Tal.: Mangaon Dist.: Raigad, M. S., Pin: 402103,	Other University Representative	Present
19.	Dr. S. D. Shirbhadurkar	Principal, Dr. D. Y. Patil Technical Campus Varale Talegaon, Pune	Pune University Representative	Present
20.	Mr. Anand S. Gharpure	Designation: Director Affiliation: Sonic Multitech Private Limited Name of Industry with address: Sonic Multitech Private Limited, Nashik	Industry Representative	Present
21.	Mr. Kiran More	Designation: General Manger (Projects) Affiliation: ProNET Technologies Pvt Ltd	Postgraduate alumnus	Present

Agenda I: Welcome Address and Introduction of Board Members

The proceedings of the meeting commenced with a formal welcome address delivered by Dr. Jayant J. Chopade, Chairman of the Board of Studies, Electronics and Telecommunication Engineering Department and Electronics & Computer Engineering.

The Chairman extended a cordial welcome to all distinguished members, experts, industry representatives, and invitees present for the meeting. He expressed sincere gratitude to all attendees for their valuable presence and participation.

Later, Dr. Jayant J. Chopade presented a comprehensive overview of the institute and the Department of Electronics and Telecommunication Engineering. The presentation highlighted the department's academic achievements, infrastructure, research activities, teaching-learning practices, student development initiatives, and industry interaction programs.

The Chairman thereafter introduced all the members and experts of the Board of Studies to the assembly, including:

- Dr. S. D. Shirbhadurkar – Representative from Savitribai Phule Pune University
- Dr. Sanjiv V. Bonde – Representative from other universities
- Dr. Sanjay L. Nalbalwar – Representative from other universities
- Mr. Anand S. Gharpure – Industry Representative
- Mr. Kirankumar More – Postgraduate Alumnus

Agenda II: Discussion and Approval of UG and PG Curriculum Structure as per NEP Guidelines

The proposed curriculum framework for the Undergraduate and Postgraduate programmes, designed in accordance with the National Education Policy (NEP) guidelines, was presented before the Board for detailed deliberation and approval.

The Board members thoroughly reviewed the proposed curriculum structure with respect to:

The members thoroughly reviewed the curriculum structure pertaining to the First Year B.Tech. Programme, including:

- Teaching Scheme
- Examination Scheme
- Credit Distribution
- Theory and Practical Components
- Academic structure pattern 2024 for Eight Semesters and course titles for semester I and II

The members appreciated the systematic and learner-centric approach adopted while designing the curriculum framework to meet academic standards and emerging industry requirements.

Resolution

After detailed discussions and deliberations, the proposed curriculum structure for both Undergraduate and Postgraduate programmes was unanimously approved and finalized by the Board of Studies.

Agenda III: Discussion and Approval of Second Year B.Tech. Curriculum

The detailed curriculum structure for the Second Year B.Tech. Programme was presented before the Board for academic review and approval. The members carried out detailed discussions regarding the course structure, contents, and academic relevance.

During the deliberations, the following suggestions and observations were made by the Board members:

1. **Mr. Anand S. Gharpure** suggested inclusion of the study of diodes and transistors in the First Year curriculum to strengthen students' fundamental understanding of electronics.

Response: The Chairman clarified that the basic concepts of diodes and transistors are already covered in the Physics syllabus of 11th and 12th standards. Further, these topics are elaborated in the Electrical Electronics Workshop to ensure comprehensive conceptual understanding.

2. **Mr. Kirankumar More** suggested incorporation of foreign language courses within the curriculum framework.

Response: Dr. V. H. Patil informed the Board that foreign language studies have already been included as audit courses from the Second Year onwards.

3. **Dr. S. D. Shirbhadurkar** queried whether all guidelines issued by the Maharashtra State Board and the National Education Policy (NEP) had been properly incorporated.

Response: It was confirmed that all prescribed guidelines and regulations of the Maharashtra State Board and NEP have been duly followed while designing the curriculum.

4. **Dr. Sanjay L. Nalbalwar** raised a query regarding the protocol applicable to students exiting after the First Year and subsequently re-joining the programme after a gap period.

Response: It was informed that the matter has been forwarded to the Academic Council for further discussion and policy-level resolution.

5. **Dr. Sanjiv V. Bonde** appreciated the overall structure and organization of the First Year syllabus.

The valuable suggestions and recommendations of the members were positively discussed during the meeting.

Resolution

It was unanimously resolved that the proposed curriculum for the Second Year B.Tech. programme be approved with minor revisions and recommendations as discussed during the meeting.

Agenda IV: Discussion and Approval of First Year M.Tech. VLSI & ES Currieulum

The proposed curriculum for the First Year M.Tech. Programme was presented before the Board for discussion and approval.

The Board members reviewed the curriculum structure, course contents, and academic framework in detail. The members appreciated the relevance and comprehensiveness of the curriculum designed in alignment with advanced technical and research-oriented requirements.

Resolution

After detailed discussion and deliberation, the proposed curriculum for the First Year M.Tech. Programme was unanimously approved by the Board of Studies.

Agenda V: Vote of Thanks and Conclusion

There being no further items on the agenda, the meeting concluded with a formal Vote of Thanks proposed by Mr. Vishwas R. Wadekar.

He expressed sincere gratitude to all Board members, academic experts, industry representatives, alumni members, and faculty members for their valuable suggestions, active participation, and significant contributions toward strengthening the academic framework of the Department of Electronics and Telecommunication Engineering.

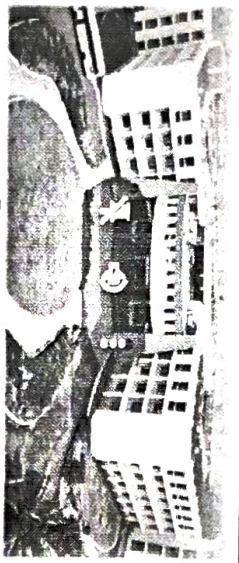


Dr. Jayant J. Chopade
Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
Matoshri College of Engineering & Research Centre

J Jayant Chopade (Presenting)



Matoshri College of Engineering and Research Centre, Eklahare, Nashik
(Autonomous)
Accredited by MAAC and NBA



Department of
Electronics & Telecommunication Engineering
Wel-Comes
You
Board of Studies Members

- J Jayant Chopade
- D Devidas Dighe
- B Bhagwan Bodke
- N B 4 others
- Dr. Ravindra Da...

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IN MEETING

Contributors 10

Dr. Ravindra Dabn... (You) Meeting host

B Bhagwan Bodke Meeting host

B Bhagwan Bodke Meeting host

D Devidas Dighe

Dr. Ravindra Dabhadre Meeting host



Jayant Chopade (Presenting)

24U0104: Basics of Electrical and Electronics Engineering: Course Contents

24U0104: Basics of Electrical and Electronics Engineering

Teaching Scheme	Credit	Examination Scheme & Marks
TH: 03 Hours/Week	03	CAT_1: 20 Marks CAT_2: 20 Marks ESE: 60 Marks

Companion Course, if any: Basic Electrical & Electronics Engineering Laboratory (24U104)

Course Outcomes: On completion of the course, students will be able to:

CO	CO Description	Level
CO1	Identify protective devices for electrical installations.	1,2
CO2	Build and test digital circuit using Universal /Bread board, Flip-Flop, Counters and Shift Registers.	1,2,3
CO3	Compare the efficiency and regulation of a single phase transformers, select meters for specific applications.	1,2,3
CO4	Understand various wire and wireless communication system and standard cellular concept GSM system.	1,2
CO5	Understand working of various Electrical and Electronics instruments.	1,2,3

Dr.Ranjit Gawande has left the meeting

Jayant Chopade

Kirankumar more

Er. Anand S. Gh...

SANJIV BONDE

17 others

Dr. Ravindra Da...

3:23 PM | nvu-eaqt-prx

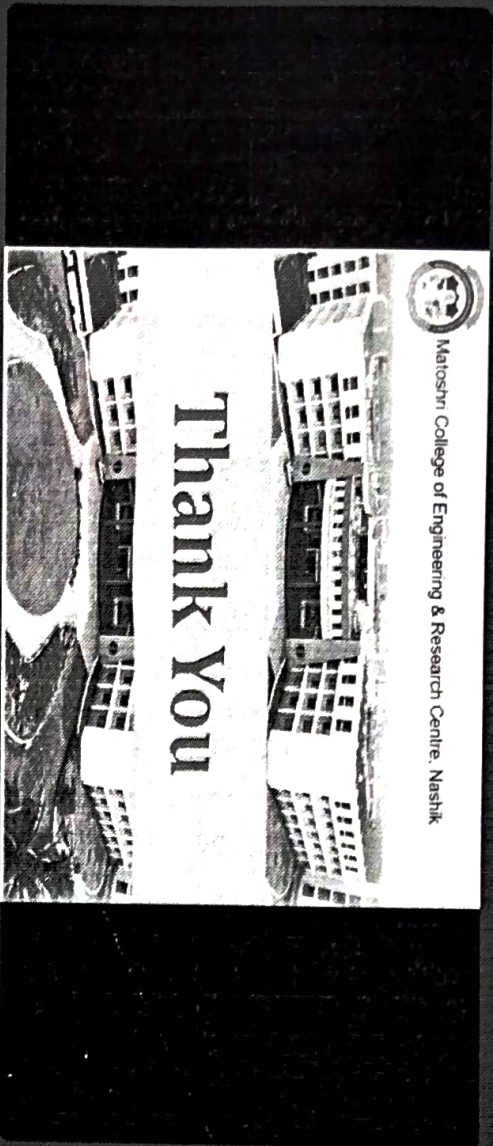
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Vidya Gite	Muted
Vishwas Wadekar	Muted



Jayant Chopade (Presenting)



Varsha Patil

SANJIV BONDE

Vishwas Wadgaonkar

Jayant Chopade

19 others

Dr. Ravindra Da...

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Contributors 25

Dr. Ravindra Dabhi... (You) Meeting host

Bhagwan Bodke Meeting host

Bhagwan Bodke Meeting host

Devidas Digne

Devidas Digne

Dinesh Adhav

Dr. Dnyaneshwar Ahire



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**Electronics and Telecommunication Engineering &
Electronics and Computer Engineering Department**

Date: 29/06/2024

Action Taken Report (ATR) on Board of Studies First Meeting

Sr. No.	Discussion / Agenda Item	Resolution	Action Taken	Status
1	Approval of UG and PG Curriculum Structure as per NEP Guidelines	The proposed curriculum structure for Undergraduate and Postgraduate programmes' was unanimously approved by the Board.	The curriculum framework was reviewed, finalized, and approved in accordance with NEP guidelines.	Complied
2	Approval of Second Year B.Tech. Curriculum	The proposed curriculum for the Second Year B.Tech. Programme was approved with minor revisions and recommendations.	Suggestions regarding foundational electronics topics, foreign language audit courses, and curriculum compliance were incorporated and documented.	Complied
3	Approval of First Year M.Tech. Curriculum	The proposed curriculum for the First Year M.Tech. Programme was unanimously approved by the Board.	The curriculum structure and course framework were finalized and forwarded for implementation.	Complied
4	Student Exit and Re-entry Policy Query	The matter regarding rejoining protocol for students exiting after the First Year was referred for further discussion.	The issue was forwarded to the Academic Council for policy-level discussion and resolution.	Under Process

Dr. Jayant J. Chopade

Chairman, Board of Studies, Head of Dept. (E&TC/E&CE)
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