



Savitribai Phule Pune University

ELECTRONICS ENGINEERING POST GRADUATE STUDENTS' PROJECT EXHIBITION
(VLSI & Embedded System, Signal processing, Digital Systems, Microwave Engg., Communication Network)
(18th – 19th June 2016)

Certificate of Participation

This is to certify that Mr./Mrs./Miss Pagare Meghana Sanjay of Sandip Institute
of Technology & Research Centre has participated in e-PG Project Exhibition (e-PGpex-2016) Organized
under the aegis of BoS (Electronics Engineering), Savitribai Phule Pune University held at Department of Electronics and Telecommunication
Engineering, MAEER's MIT College of Engineering, Pune-38 and JSPM's Rajarshi Shahu College of Engineering, Tathawade, Pune-33.

Prof. (Dr.) M.S. Nagmode
Principal, MITCOE, Pune

Prof. (Dr.) D.S. Bormane
Coordinator, BOS Electronics

Prof. (Dr.) Y.P. Nerkar
Coordinator, Faculty of Engineering