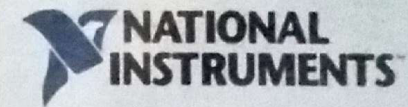




EMBEDDED SYSTEMS
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SANDIP INSTITUTE OF TECHNOLOGY AND RESEARCH CENTRE

Mahiravani, Trimbak Road, Nashik, (M.S), India

Department of Electronics and Telecommunication Engineering

Two Days Workshop on

“LabVIEW: Graphical System Design in Engineering Applications”

Certificate

This is to certify that Pagare Meghana Sanjay
of P.G Student, SITRC, Nashik has participated in Two days workshop on
“LabVIEW: Graphical System Design in Engineering Applications” organized by Department of E&TC, SITRC in association
with IEI (India), Embedded System Solutions Pvt Ltd. & National Instruments, Bangalore on 21st & 22nd August, 2015.

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