

Matoshri College of Engineering and Research Centre, Eklahare, Nashik

Department of Electrical Engineering

Minutes of Meeting (MoM)

Board of Studies (1st)

Date: 20th July 2024

The first meeting of the Board of Studies (BoS) at Department of Electrical Engineering of Matoshri College of Engineering and Research Centre, Nashik was conducted on 16th July 2024 at 03.00 PM in online mode.

Following members were present for the meeting.

1. Dr. Khule Shridhar S.	2. Dr. Munje Ravindra K.	3. Dr. Bodkhe Sanjay B.
4. Dr. Mohod Sharad W.	5. Dr. Mandlik Manoj	6. Mrs. Pawaskar Tejaswini R.
7. Dr. Patil Varsha H.	8. Dr. Ahire Dnyaneshwar D.	9. Mr. Aranke Vivek R.
10. Mr. Hadpe Somnath S.	11. Mr. Jagtap Anil E.	12. Ms. Kokne Saroj B.
13. Ms. Jane Kunjal C.	14. Mr. Nirgude Amol S.	15. Mr. Bhise Digamber R.
16. Ms. Kare Vinita V.	17. Ms. Kumbharde Pooja V.	18. Ms. Deshmukh Priyanka V.
19. Mr. Pawaskar Rupesh C.		

Item 1: About Institute and Department

Discussion:

Dr. Shridhar S. Khule, Chairman of the Board of Studies, warmly welcomed all the members to the first BoS meeting. He delivered an introductory address highlighting the vision, mission, academic activities, and achievements of the Department of Electrical Engineering and provided an overview of the Institute.

Item 2: Grant of Autonomy Status

Discussion:

Dr. Shridhar S. Khule formally introduced all the Board of Studies members and welcomed them to the committee. He explained the constitution, objectives, and responsibilities of the BoS and emphasized its role in designing an industry-oriented and outcome-based curriculum.

Item 3: Introduction and Welcome of BOS Members

Discussion:

Dr. Shridhar S.Khule welcomed all members and introduced the Board of Studies (BoS) members. He provided a formal introduction of the BoS members and discussed the purpose of the BoS.

Item 4: Discussion on Proposed Curriculum Structure adapting NEP guidelines

Discussion:

The proposed curriculum structure for the Bachelor of Technology (B.Tech.) and Master of Technology (M.Tech.) programmes, to be implemented from the Academic Year 2024–25, was presented before the Board Members. Detailed discussions were held regarding the curriculum structure of the First Year B.Tech. programme, including the teaching scheme, examination scheme, course credits, and semester-wise course distribution. Dr. Varsha Patil provided valuable insights into the Liberal Learning courses, highlighting student development activities such as sports, gymnasium, and other co-curricular components aligned with the National Education Policy (NEP). The members actively participated in the discussion and suggested several measures to enhance interdisciplinary learning and student employability.

Resolution: It was unanimously resolved to prepare multidisciplinary minor course verticals for students from the Second Year to the Fourth Year. The Department will also propose interdisciplinary Open Elective courses for approval by the Academic Council.

Item 5: Discussion on Basic Electrical and Electronics Engineering course and Electrical

Workshop at First Year

Discussion:

The syllabus of the **Basic Electrical and Electronics Engineering** course and the **Electrical Workshop** laboratory course was presented before the Board for review. Dr. Shridhar S. Khule explained the course objectives, course outcomes, syllabus contents, learning resources, and laboratory implementation guidelines for both theory and practical components. The BoS members thoroughly reviewed the proposed syllabus and provided constructive suggestions. Dr. Sharad W. Mohod recommended reducing the syllabus content to ensure that the entire course could be effectively covered within the prescribed instructional hours without compromising learning outcomes.

Resolution: It was resolved to revise the syllabus of the Basic Electrical and Electronics Engineering course by rationalizing its contents. The suggestions and recommendations made by the BoS members will be incorporated and submitted to the Academic Council for further consideration and approval.

As there were no further items for discussion, the meeting concluded with a formal vote of thanks proposed by the Chair. The Chair expressed sincere appreciation to all the members for their valuable suggestions, active participation, and support in strengthening the academic framework of the Department.

A handwritten signature in blue ink, appearing to read 'Dr. Khule Shridhar S.', is positioned above the printed name.

Dr. Khule Shridhar S.
Chairman,
Board of Studies - Electrical Engineering `

Matoshri Education Society's
Matoshri College of Engineering and Research Centre, Eklahare, Nashik
Department of Electrical Engineering
Board of Studies (1st)
Action Taken Report (ATR)

Item No	Agenda Point	Action Taken
4	Discussion on Proposed Curriculum Structure adapting NEP guidelines Discussion	Verticals for multidisciplinary minor courses are prepared
5	Discussion on Basic Electrical and Electronics Engineering course and Electrical Workshop at First Year	Contents of Basic Electrical and Electronics course is revised.



Dr. Khule Shridhar S.
Chairman,
Board of Study - Electrical Engineering

Place: Eklahare, Nashik

Date: 08th August 2024

Meeting Photograph

The image is a collage of six screenshots from a Google Meet session, arranged in a 3x2 grid. The top row shows a presentation slide with a table of Semester I courses and their credits, and another slide with two tables: 'Table 1: Total Credits and Total Marks for Bachelor of Technology (BTech)' and 'Table 2: Nonexclusion for Course Codes'. The bottom row shows a grid of participants' video feeds and a presentation slide titled '2401116: Electronics and Electrical Workshop'.

Semester I Courses Table (Top Left Screenshot):

Course Code	Course Title	Teaching Scheme (L:T:P)	Exam Scheme (T:R)	Credits
2401001	Engineering Mathematics I	3:1:0	20:10	3
2401002	Engineering Mathematics II	3:1:0	20:10	3
2401003	Engineering Mathematics III	3:1:0	20:10	3
2401004	Engineering Mathematics IV	3:1:0	20:10	3
2401005	Engineering Mathematics V	3:1:0	20:10	3
2401006	Engineering Mathematics VI	3:1:0	20:10	3
2401007	Engineering Mathematics VII	3:1:0	20:10	3
2401008	Engineering Mathematics VIII	3:1:0	20:10	3
2401009	Engineering Mathematics IX	3:1:0	20:10	3
2401010	Engineering Mathematics X	3:1:0	20:10	3
2401011	Engineering Mathematics XI	3:1:0	20:10	3
2401012	Engineering Mathematics XII	3:1:0	20:10	3
2401013	Engineering Mathematics XIII	3:1:0	20:10	3
2401014	Engineering Mathematics XIV	3:1:0	20:10	3
2401015	Engineering Mathematics XV	3:1:0	20:10	3
2401016	Engineering Mathematics XVI	3:1:0	20:10	3
2401017	Engineering Mathematics XVII	3:1:0	20:10	3
2401018	Engineering Mathematics XVIII	3:1:0	20:10	3
2401019	Engineering Mathematics XIX	3:1:0	20:10	3
2401020	Engineering Mathematics XX	3:1:0	20:10	3

Table 1: Total Credits and Total Marks for Bachelor of Technology (BTech)

Semester	Total Credits	Total Marks
I	22	700
II	22	700
III	22	700
IV	22	700
V	22	700
VI	22	700
VII	22	700
VIII	22	700
Total	176	5600

Table 2: Nonexclusion for Course Codes

Course Code	Excluded Course Code
2401001	2401002
2401002	2401001
2401003	2401004
2401004	2401003
2401005	2401006
2401006	2401005
2401007	2401008
2401008	2401007
2401009	2401010
2401010	2401009
2401011	2401012
2401012	2401011
2401013	2401014
2401014	2401013
2401015	2401016
2401016	2401015
2401017	2401018
2401018	2401017
2401019	2401020
2401020	2401019

2401116: Electronics and Electrical Workshop

Workshop Scheme

Course Code	Excluded Course Code
2401116	2401117
2401117	2401116

Workshop Objectives

- To understand the fundamentals of electronics components and their functions.
- To understand the safety measures and their application in the laboratory.
- To understand the safety measures and their application in the laboratory.
- To understand the safety measures and their application in the laboratory.
- To understand the safety measures and their application in the laboratory.

Workshop Outcomes

- Students will be able to identify and name the electronic components.
- Students will be able to identify and name the electronic components.
- Students will be able to identify and name the electronic components.
- Students will be able to identify and name the electronic components.
- Students will be able to identify and name the electronic components.